

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2154	(714/3 714/8 714/25).ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/12/13 10:09
L2	58	L1 and (redundan\$3 with logic)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/12/13 10:09
L3	9974	(714/3 714/8 714/25 714/48 714/723 714/724 714/725 714/726 714/727 714/729 714/736 714/741 714/742 714/746 714/759 714/799 714/764 703/28).ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/12/13 10:11
L4	2	(external adj in adj circuit adj emulator)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/12/13 10:11
L5	16	(external adj ("in-circuit") adj emulator)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/12/13 10:11
L6	2154	(714/3 714/8 714/25).ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/12/13 10:11
L7	58	L6 and (redundan\$3 with logic)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/12/13 10:11
L8	762	(L3 or L4 or L5 or L7) and (pld fpga)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/12/13 10:11
L9	554	L8 and (logic with (FPGA pld))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/12/13 10:11
L10	20	L9 and (logic with correct\$3 with (error fault))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/12/13 10:16
L12	1	(fpga and (defective adj logic) and scan\$4 and correct\$3).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2007/12/13 10:17